

### **FEATURES**

- Ultra-wide 4:1 input voltage range
- High efficiency up to 88%
- No-load power consumption as low as 0.12W
- I/O test isolation voltage: 1.5k VDC
- Input under-voltage protection, output short-circuit, over-current, over-voltage protection
- Operating ambient temperature range: -40°C to +85°C
- Meet CISPR32/EN55032 CLASS A, without extra components
- Input reverse polarity protection available with chassis(A2S) or Din-Rail mounting (A4S) version
- Industry standard pin-out

UL60950-1 URA\_YMD-6WR3 & URB\_YMD-6WR3 series are isolated 6W DC-DC converter products with 4:1 input voltage range. They feature efficiencies up to 88%, 1500VDC input to output isolation and the converter safely operate ambient temperature of -40°C to +85°C, input under-voltage protection, output short-circuit, over-current, over-voltage protection. They meet CLASS A of CISPR32/EN55032 EMI standards without external components, optional packages are offered for chassis or DIN-rall mounting (A2S, A4S), adding additional input reverse polarity protection, which make them widely applied in medical care, industrial control, electric power, instruments and communication and railway fields.

	Part No.®	Input Volta	ige (VDC)	Output		Full Load	Capacitive
Certification		Nominal <sup>®</sup> (Range)	Max.®	Voltage (VDC)	Current (mA) Max./Min.	Efficiency® (%) Min./Typ.	Load <sup>®</sup> (µF)Max.
	URA2405YMD-6WR3		40	±5	±600/0	81/83	470
	URA2412YMD-6WR3			±12	±250/0	84/87	100
	URA2415YMD-6WR3			±15	±200/0	83/85	100
	URA2424YMD-6WR3			±24	±125/0	85/87	100
	URB2403YMD-6WR3	24 (9-36)		3.3	1500/0	75/77	1800
	URB2405YMD-6WR3			5	1200/0	80/83	1000
	URB2409YMD-6WR3			9	667/0	82/84	680
	URB2412YMD-6WR3			12	500/0	83/85	470
UL/EN/BS EN/IEC	URB2415YMD-6WR3			15	400/0	84/86	220
OL/EIN/B3 EIN/IEC	URB2424YMD-6WR3			24	250/0	84/86	100
	URA4805YMD-6WR3			±5	±600/0	81/83	470
	URA4812YMD-6WR3			±12	±250/0	85/87	100
	URA4815YMD-6WR3			±15	±200/0	86/88	100
	URB4803YMD-6WR3	48	80	3.3	1500/0	77/79	1800
	URB4805YMD-6WR3	(18-75)	OU	5	1200/0	81/83	1000
	URB4812YMD-6WR3			12	500/0	85/87	470
	URB4815YMD-6WR3			15	400/0	86/88	220
	URB4824YMD-6WR3			24	250/0	86/88	100

#### Notes:

- ①Use "A2S" suffix for chassis mounting and "A4S" suffix for Din-Rail mounting;
- ②The A2S and A4S Model's start-up and minimum input voltages are increased by 1VDC due to the input reverse polarity protection circuit;
- ③Exceeding the maximum input voltage may cause permanent damage;
- (4) Efficiency is measured at nominal input voltage and rated output load; efficiencies for A2S and A4S Model's is decreased by 2% due to the input reverse polarity protection circuit;
- The specified maximum capacitive load value for Vo1 and Vo2 output is identical.











Input Specifications						
Item	Operating Conditions		Min.	Тур.	Max.	Unit
	24VDC nominal input series,	3.3V output	_	268/5	275/12	
Input Current (full load / no-load)	nominal input voltage	Others	-	301/5	312/12	
	48VDC nominal input series, nominal input voltage	3.3V output	_	130/4	134/8	mA
		Others	-	150/4	155/8	1
Reflected Ripple Current	Nominal input voltage	-	20			
0	24VDC nominal input series		-0.7	-	50	VDC
Surge Voltage (1sec. max.)	48VDC nominal input series		-0.7	-	100	
Character van Valderara	24VDC nominal input series			-	9	
Start-up Voltage	48VDC nominal input series		-	-	18	
Input Under Veltage Pretection	24VDC nominal input series	nominal input series		6.5		
Input Under-voltage Protection	48VDC nominal input series		12	15.5		
Input Filter			Pi f	ilter		
Hot Plug				Unavo	ailable	

<b>Output Specifications</b>						
Item	Operating Conditions		Min.	Тур.	Max.	Unit
Voltage Accuracy®	0% - 100% load			±1	±3	
Lineary Dear Jarkier	Input voltage variation from	Vo1		±0.2	±0.5	
Linear Regulation	low to high at full load	Vo2	-	±0.5	±1	
	E9/ 1009/ la sud	Vo1		±0.5	±1	<b>%</b>
Load Regulation®	5% -100% load	Vo2		±0.5	±1.5	
Cross Regulation	Dual outputs, Vo1 load at 50° of 10% - 100%		_	±5		
Transient Recovery Time				300	500	μs
Translant Dassaus Davidation	25% load step change, nominal input voltage	3.3V, 5V, ±5V output		±5	±8	%
Transient Response Deviation		Others		±3	±5	
Temperature Coefficient	Full load				±0.03	%/°C
Ripple & Noise®	20MHz bandwidth, 5% - 100%	load		60	85	mV p-p
Over-voltage Protection					160	%Vo
Over-current Protection	Input voltage range	_	110	140	190	%lo
Short-circuit Protection		_		Continuous,	self-recovery	

Note: ①Output voltage accuracy of ±5VDC/±9VDC output converter for 0%-5% load is ±5% max; 2Load regulation for 0%-100% load is ±5%;

③Under 0% -5% load conditions, ripple & noise does not exceed 5%Vo. The "parallel cable" method is used for Ripple and Noise test, please refer to DC-DC Converter Application Notes for specific information.

Item	Operating Conditions	Min.	Тур.	Max.	Unit
Isolation	Input-output Electric Strength Test for 1 minute with a leakage current of 1mA max.	1500	-		VDC
Insulation Resistance	Input-output resistance at 500VDC	1000			MΩ
Isolation Capacitance	Input-output capacitance at 100kHz/0.1V	-	1000		pF
Operating Temperature	See Fig. 1	-40		+85	°C
Storage Humidity	Non-condensing	5		95	%RH
Storage Temperature		-55		+125	
Pin Soldering Resistance Temperature	Soldering spot is 1.5mm away from case for 10 seconds		-	+300	င
Vibration		IEC/EN61373 - Category 1, Grade B			
Switching Frequency *	PWM mode		300		kHz
MTBF	MIL-HDBK-217F@25°C	1000			k hours







Mechanical Specifications					
Case Material	Aluminum alloy				
	Horizontal package	25.40 x 25.40 x 11.70 mm			
Dimensions	A2S chassis mounting	76.00 x 31.50 x 21.20 mm			
	A4S DIN-rail mounting	76.00 x 31.50 x 25.80 mm			
Weight	Horizontal package/A2S wiring package/A4S rail package	12.5g /36.0g /56.0g(Typ.)			
Cooling method	Free air convection				

Electron	nagnetic Comp	atibility (EMC)		
Emissions	CE	CISPR32/EN55032	CLASS A (without extra components)/ CLASS B (see Fig.3-② for recommended circuit)	
ETTISSIOTIS	RE	CISPR32/EN55032	CLASS A (without extra components)/ CLASS B (see Fig.3-② for recommended circuit)	
	ESD	IEC/EN61000-4-2	Contact ±4kV	perf. Criteria B
	RS	IEC/EN61000-4-3	10V/m	perf. Criteria A
	EFT	IEC/EN61000-4-4	±2kV (see Fig.3-① for recommended circuit)	perf. Criteria B
Immunity	Surge	IEC/EN61000-4-5	line to line ±2kV (see Fig.3-①for recommended circuit)	perf. Criteria B
ITHITICITITY	CS	IEC/EN61000-4-6	3 Vr.m.s	perf. Criteria A
	Voltage dips, short interruptions and voltage variations immunity	IEC/EN61000-4-29	0%,70%	perf. Criteria B

Electromagnetic Compatibility (EMC) (EN50155)					
	CE	EN50121-3-2 150kHz-500kHz 99dBuV (see Fig.3-2) for recommended circuit)			
Emissions	CE	EN55016-2-1 500kHz-30MHz 93dBuV (see Fig.3-2) for recommended circuit)			
ETTISSIOTIS	RE	EN50121-3-2 30MHz-230MHz 40dBuV/m at 10m (see Fig.3-2) for recommended a			
	RE	EN55016-2-1 230MHz-1GHz 47dBuV/m at 10m (see Fig.3-2) for recommended	circuit)		
	ESD	EN50121-3-2 Contact ±6kV/Air ±8kV	perf. Criteria A		
	RS	EN50121-3-2 20V/m	perf. Criteria A		
Immunity	EFT	EN50121-3-2 ±2kV 5/50ns 5kHz (see Fig.3-① for recommended circuit)	perf. Criteria A		
	Surge	EN50121-3-2 line to line ±1kV (42 $\Omega$ , 0.5 $\mu$ F) (see Fig.3-① for recommended circuit)	perf. Criteria A		
	CS	EN50121-3-2 0.15MHz-80MHz 10V r.m.s	perf. Criteria A		

# Typical Characteristic Curve

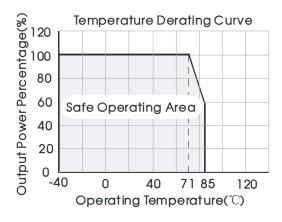
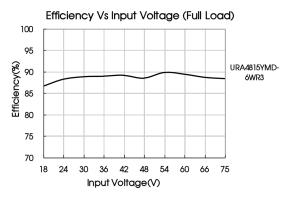
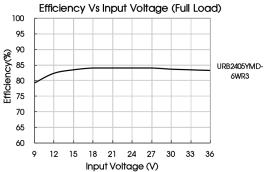


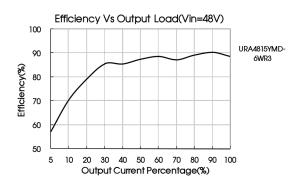
Fig. 1

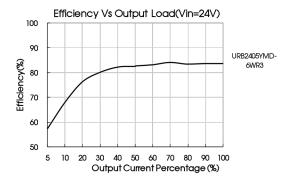








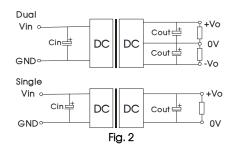




## **Design Reference**

### 1. Typical application

All the DC/DC converters of this series are tested before delivery using the recommended circuit shown in Fig. 2. Input and/or output ripple can be further reduced by appropriately increasing the input & output capacitor values Cin and Cout and/or by selecting capacitors with a low ESR (equivalent series resistance). Also make sure that the capacitance is not exceeding the specified max. capacitive load value of the product.



Vin(VDC)	Cin	Cout
24	100µF/50V	10µF/50V
48	10 μF- 47μF/100V	10µF/50V

#### **EMC** compliance circuit

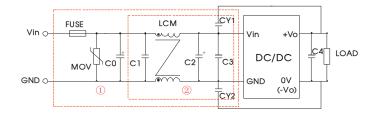


Fig. 3

Notes: For EMC tests we use Part ① in Fig. 3 for immunity and part ② for emissions test. Selecting based on needs.

#### Parameter description:

	lorer description:							
Model	Vin: 24VDC	Vin: 48VDC						
FUSE	Choose according to actual input current							
MOV	S20K30 S14K60							
C0	680µF/50V	680µF/100V						
C1	1µF/50V	1µF/100V						
C2	330µF/50V 330µF/100V							
C3	4.7µF/50V	4.7µF/100V						
C4	Refer to the Cout in Fig.2							
LCM	4.7mH							
CY1/CY2	lnF/2	1nF/2kV						



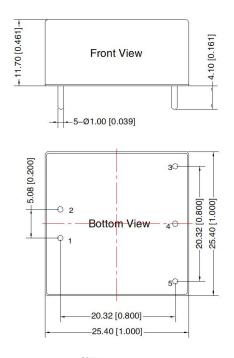




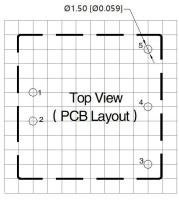


3. The products do not support parallel connection of their output

## Dimensions and Recommended Layout



THIRD ANGLE PROJECTION Ø1.50 [Ø0.059]-



Note: Grid 2.54\*2.54mm

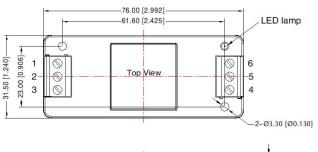
Pin-Out					
Pin	Single	Dual			
1	GND	GND			
2	Vin	Vin			
3	+Vo	+Vo			
4	No Pin	VO			
5	OV	-Vo			

Note: Unit: mm[inch] PIN1/2/3/4/5: \$ 1.0mm

Pin diameter tolerances:  $\pm 0.10[\pm 0.004]$ General tolerances:  $\pm 0.50[\pm 0.020]$ 

## URA\_YMD-6WR3A2S & URB\_YMD-6WR3A2S Dimensions





Front View	21.20 [0.835]
	8.80 [0.346]-
	Front View

Pin-Out						
Pin	1	2	3	4	5	6
Dual	NC	GND	Vin	+Vo	OV	-Vo
Single	NC	GND	Vin	+Vo	NC	OV

Note:

Unit: mm[inch]

Wire range: 24-12 AWG

Tightening torque: Max 0.4 N • m General tolerances:  $\pm 1.00[\pm 0.039]$ 



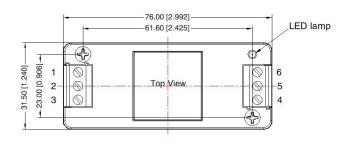




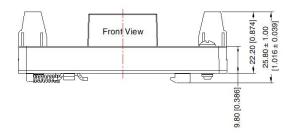


# URA\_YMD-6WR3A4S & URB\_YMD-6WR3A4S Dimensions





Pin-Out						
Pin	1	2	3	4	5	6
Dual	NC	GND	Vin	+Vo	OV	-Vo
Single	NC	GND	Vin	+Vo	NC	OV



Note:

Unit: mm[inch] Mounting rail: TS35 Wire range: 24-12 AWG

Tightening torque: Max 0.4 N • m General tolerances:  $\pm 1.00[\pm 0.039]$ 

2021.12.27-B/1

Page 6 of 6







